

WHAT IS CLAIMED IS:

1. A signal production circuit for producing, from digital data, a plurality of kinds of pulse signals which are respectively repetitions of a predetermined sequence of pulses, comprising:

storage means for storing, as the digital data, a single signal of serial data including a time series of data pulses representative of all rise and fall timings of the plurality of kinds of pulse signals and data pulses representative of all time intervals between the rise and fall timings; and

serial-to-parallel converter means for reading the signal of serial data from the storage means and producing, as parallel data, the plurality of kinds of pulse signals from the data representative of all the predetermined rise and fall timings of the plurality of kinds of pulse signals.

2. The signal production circuit as defined in claim 1, wherein

the serial-to-parallel converter means includes a plurality of flip-flops, connected in cascade so that an output signal of one flip-flop is an input signal of a next, which convert data from serial to parallel by

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the serial-to-parallel converter means performs an AND operation between the signal of serial data and data pulses with a pulse period equal to, or shorter than, a

8. The signal production circuit as defined in claim 1,

wherein:

the single signal of serial data includes a time series of data pulses each of which rises in synchronism with at least one of rise and fall timings of the plurality of kinds of pulse signals.

9. A display device including a signal production circuit for producing, from digital data, a plurality of kinds of pulse signals which are respectively repetitions of a predetermined sequence of pulses,

the signal production circuit comprising:

storage means for storing, as the digital data, a single signal of serial data including a time series of data pulses representative of all rise and fall timings of the plurality of kinds of pulse signals and data pulses representative of all time intervals between the rise and fall timings; and

serial-to-parallel converter means for reading the signal of serial data from the storage means and producing, as parallel data, the plurality of kinds of pulse signals from the data representative of all the predetermined rise and fall timings of the plurality of kinds of pulse signals.

10. The display device as defined in claim 9, further

display pixels which are constituted by electroluminescence elements.

wherein

12. The display device as defined in claim 11, further comprising a write drive circuit for sequentially applying to display pixels write voltage which is necessary for the display pixels to emit light,

wherein the signal production circuit supplies plurality of first timing signals to a write drive circuit as the control signals to control an application timing of the write voltage.

13. The display device as defined in claim 11, further comprising a modulation drive circuit for applying to display pixels modulation voltage which turns on or off the display pixels according to display data,

wherein

the signal production circuit supplies to the modulation drive circuit a plurality of second timing signals as control signals to control timings of application of the modulation voltage.

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